Amendments To the Claims

Claim 1 (Currently amended):   A chip resistor being symmetrical to allow for direct

loading to a pick-and-place machine without concern for top-bottom orientation, comprising:

a substantially flat substrate having opposite parallel symmetrical top and bottom surfaces, and a

    central longitudinal plane of symmetry;

separate and spaced first and second resistive layers on the top and bottom surfaces, respectively,

    electrically connected in parallel to each other; and

the top and bottom surfaces of the substrate being symmetrically located with respect to and

    equidistant from the central longitudinal plane so that when electrical current passes

    through the resistive layers, a temperature distribution within the substrate will be

    substantially symmetrical about the central longitudinal plane of the substrate for

    eliminating thermal bending thereof;

wherein an area of the first resistive layer is substantially equal to an area of the second resistive

    layer such that the chip resistor with both resistive layers tolerates higher instantaneous

    pulsed power than either layer could provide separately and individually;

first and second terminals for surface mounting, each terminal being electrically connected to the

    first and second resistive layers, the terminals being substantially symmetrical about the

    central longitudinal plane; and

the substrate, resistive layers, and terminals are symmetrical about the central longitudinal plane

    to allow for direct loading to the pick-and-place machine without concern for top-bottom

    orientation.

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Claim 2 (Original):   The chip resistor of claim 1 wherein the first resistive layer and the second resistive layer are thick film resistive layers.

Claim 3 (Original):   The chip resistor of claim 1 wherein the first resistive layer and the second resistive layer are thin film resistive layers.

Claim 4 (Original):   The chip resistor of claim 1 wherein the first resistive layer and the second resistive layer are foil resistive layers.

Claims 5-8 (Previously Canceled).

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